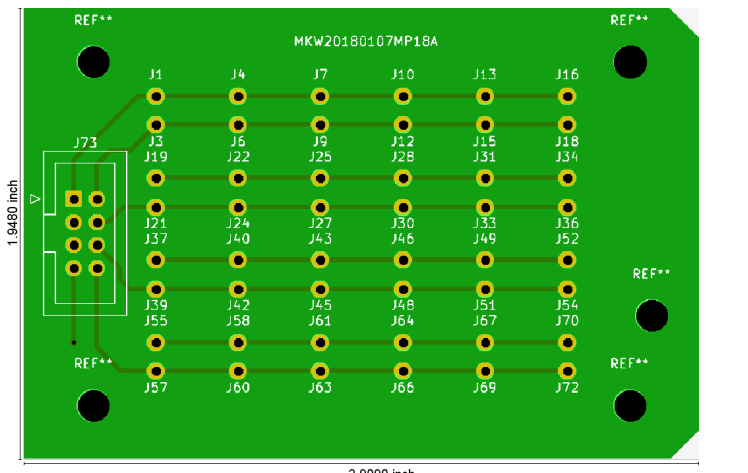
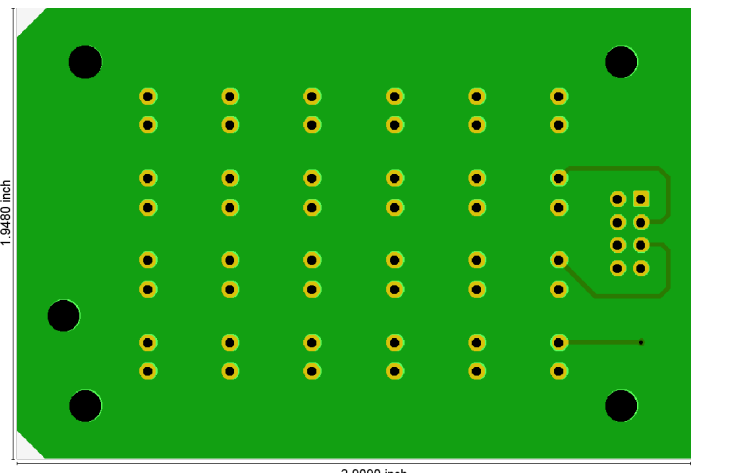


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Single PCB View - Original

Top View	Bottom View
	

Summary - General - Original

PCB Size	2.9090 inch x 1.9480 inch	Surface Finish	unknown
PCB Thickness	62.00 mil	Max. Aspect Ratio on PTH	3.9
Customer Panel Size		Number of Nets	8
Copper Layers	2	Electrical Test	Single Sided
Solder Mask	Both	Drilled SMD Pads	No
Solder Mask Color	Green	SMD Pads Top	0
Legend	Top Only	SMD Pads Bottom	0
Legend Color	White	BGA Pads Top	0
Peeloff Mask	None	BGA Pads Bottom	0
Carbon Mask	None	Drill Hole Density	11 Holes/inch ²
Edge Connectors	No		

Summary - Copper Layer Minima - Original

Layer Type	Copper Width	Critical Copper Width	Trace Width	Critical Trace Width	Copper to Copper Clr.	Trace to Trace	Same Net Clr.	Ring	Plated	Copper to NPTH Clr.	Copper to Outline Clr.
	mil	mil	mil	mil	mil	mil	mil	mil	mil	mil	mil
Outer	>16.00	>16.00	>16.00	>16.00	¹ 27.40	>20.00	>20.00	² 7.90	>32.00	>64.00	>64.00

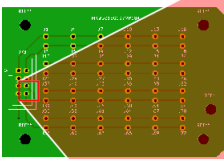
Summary - Sequences - Original

Type	Sequences	Tools	Min. End Dia.	Max. End Dia.	Holes	Moves	Min. Ring on Outer	Min. Ring on Inner	Min. Hole to Copper Clr.
			mil	mil			mil	mil	mil
PTH	1	2	15.70	39.40	57	0	7.90		>32.00
NPTH	1	2	137.80	141.70	5	0	>32.00		>64.00
Total	2	4	15.70	141.70	62	0	7.90		>64.00

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Summary Minimum Design Characteristics - Locations - Original

1

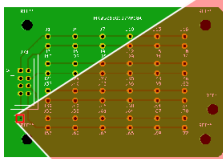


MKW20180107MP18A PCB-F_Cu_gtl
 ϕ x: 3381.54 mil
y: 4150.01 mil

Copper to Copper Clr.
Outer Layers
27.40 mil

200 mil

2



MKW20180107MP18A PCB-F_Cu_gtl
 ϕ x: 3248.19 mil
y: 3797.17 mil

Ring
Outer Layers
7.90 mil

100 mil

Stackup - Original



Pressing Stages	1
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Copper Layer Minima & Area - Original

File	Pos.	Copper Width	Critical Copper Width	Trace Width	Critical Trace Width	Copper to Copper Clr.	Same Net Clr.	Copper Area	
		mil	mil	mil	mil	mil	mil	inch ²	%
MKW20180107MP18A PCB-F_Cu_gtl	1	>16.00	>16.00	>16.00	>16.00	27.40	>20.00	0.4876	9
MKW20180107MP18A PCB-B_Cu_gbl	2	>16.00	>16.00	>16.00	>16.00	>32.00	>20.00	0.2559	5

Copper Layer Minima - Copper vs Drill - Original

File	Pos.	Ring					Copper vs Drill Clr.		Copper to Outline Clr.			
		Overall	Via	Laser Via	Comp.	Mech.	Plated	NPTH	Overall	Pad to Outline	Track to Outline	Region to Outline
		mil	mil	mil	mil	mil	mil	mil	mil	mil	mil	mil
MKW20180107MP18A PCB-F_Cu_gtl	1	7.90	7.90		13.76		>32.00	>64.00	>64.00	>64.00	>64.00	>64.00
MKW20180107MP18A PCB-B_Cu_gbl	2	7.90	7.90		13.73		>32.00	>64.00	>64.00	>64.00	>64.00	>64.00

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Drill Tools - Original

File	Tool Nr.	Span	Type	Method	Filled Via	Counter	Dia.	Tol. Min	Tol. Plus	Holes (in PCB)	Moves (in PCB)	Double Hits (in File)	Predrill Hits (in File)
							mil	mil	mil				
MKW20180107MP18A PCB_drl	1	1-2	PTH	unknown	unknown	unknown	15.70	0.00	0.00	1	0	0	0
MKW20180107MP18A PCB_drl	2	1-2	PTH	unknown	unknown	unknown	39.40	0.00	0.00	56	0	0	0
MKW20180107MP18A PCB_drl	3	1-2	NPTH	unknown	unknown	unknown	137.80	0.00	0.00	4	0	0	0
MKW20180107MP18A PCB_drl	4	1-2	NPTH	unknown	unknown	unknown	141.70	0.00	0.00	1	0	0	0

Drill Tools - Drill vs Copper - Original

File	Tool Nr.	Span	Type	Method	Dia.	Min. Ring on Outer	Min. Ring on Inner	Min. Pad Size
					mil	mil	mil	mil
MKW20180107MP18A PCB_drl	1	1-2	PTH	unknown	15.70	7.90		31.50
MKW20180107MP18A PCB_drl	2	1-2	PTH	unknown	39.40	13.73		66.86
MKW20180107MP18A PCB_drl	3	1-2	NPTH	unknown	137.80	>32.00		
MKW20180107MP18A PCB_drl	4	1-2	NPTH	unknown	141.70	>32.00		

Sequences - Original

Span	Type	Tools	Min. End Dia.	Max. End Dia.	Holes	Min. Ring on Outer	Min. Ring on Inner	Min. Hole to Copper Clr.	Min. Hole to Outline Clr.	Min. Slot to Outline Clr.
			mil	mil		mil	mil	mil	mil	mil
1-2	PTH	2	15.70	39.40	57	7.90		>32.00	194.64	disabled
1-2	NPTH	2	137.80	141.70	5	>32.00		>64.00	135.55	disabled
All	All	4	15.70	141.70	62	7.90		>64.00	135.55	disabled

Rout Tools - Original

File	Tool Nr.	Type	Tool Dia.	End Dia.	Draw Length	Nibble Count
			mil	mil	mil	

Routed Holes - Original

File	Hole Nr.	Instances	X Size	Y Size	Draw Length	Nibble Count
			mil	mil	mil	

Solder Mask - Original

Side	Min. Mask to Mask Clr.	Min. Web	Min. Ring on Cu Defined Pads	Min. Ring on SM Defined Pads	Min. Mask to Copper Clr.	Fully Covered Via Holes	Partly Covered Via Holes	Half Mask Vias
	mil	mil	mil	mil	mil			
Top	>10.00	>10.00	1.97	>10.00	>10.00	Yes	No	
Bottom	>10.00	>10.00	1.97	>10.00	>10.00	Yes	No	
Both	>10.00	>10.00	1.97	>10.00	>10.00	Yes	No	No

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Files - Original

Initial	Renamed	Format	Function	Position	Color
MKW20180107MP18A PCB-F_SilkS.gto	MKW20180107MP18A PCB-F_SilkS_gto	ger274x	silk	top	white
MKW20180107MP18A PCB-F_Mask.gts	MKW20180107MP18A PCB-F_Mask_gts	ger274x	mask	top	green
MKW20180107MP18A PCB-F_Cu.gtl	MKW20180107MP18A PCB-F_Cu_gtl	ger274x	outer	1	
MKW20180107MP18A PCB-B_Cu.gbl	MKW20180107MP18A PCB-B_Cu_gbl	ger274x	outer	2	
MKW20180107MP18A PCB-B_Mask.gbs	MKW20180107MP18A PCB-B_Mask_gbs	ger274x	mask	bottom	green
MKW20180107MP18A PCB.drl	MKW20180107MP18A PCB_drl	excellon2	mixed	1-2	
MKW20180107MP18A PCB-B_Paste.gbp	MKW20180107MP18A PCB-B_Paste_gbp	ger274x	empty	none	
MKW20180107MP18A PCB-B_SilkS.gbo	MKW20180107MP18A PCB-B_SilkS_gbo	ger274x	empty	none	
MKW20180107MP18A PCB-Edge_Cuts.gm1	MKW20180107MP18A PCB-Edge_Cuts_gm1	ger274x	cad_outline	none	
MKW20180107MP18A PCB-F_Paste.gtp	MKW20180107MP18A PCB-F_Paste_gtp	ger274x	empty	none	
MKW20180107MP18A PCB-job.gbrjob		text	document		

Comments - Original

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