

Fig: Co Nanowire deposited at 3 mA.cm⁻² current density and at Room temperature

Fig: Co Nanowire deposited at 7 mA.cm⁻² current density and at Room temperature



Figure 3.7: XRD spectra of Co nanowire deposited at 40 °C; (a) 3 mA.cm⁻², (b) 5

mA.cm⁻² and (c) 7 mA.cm⁻²







Figure 3.11: TEM Image of [001] Co nanowire deposited at 7mA.cm⁻² and 40 °C. Inset is



showing the SAED pattern which confirms the single-crystalline nature of Co nanowire

Figure 3.15: HRTEM image is showing d-spacing (0.191 nm) of dominant (101) plane. We can see (110) plane also in the figure with d-spacing of 0.125 nm



Some Modeling results



Figure 5.3: The computational domain of microfluidic chip with mesh (Finer







Figure. Various dislocations after yielding (10% strain) in Ni nanowire. (a) CSP of deformed nanowire. (b) Dislocation types present in Nickel nanowire at same strain



Details: https://www.ingentaconnect.com/content/asp/asem/2019/00000011/00000012/art00004