

- DEVS –Discrete Event System Specification
- TRNSYS Transient System Simulation Tool
- ABD –Abstract Block Diagram
- PIDO Process Integration and Design **Optimization**
- MoDAF/DoDAF Department of Defense Architecture Framework
- VHDL-AMS Very High Speed Integrated Circuit Hardware Description Language, mixed-signal extensions
- MES Manufacturing Execution System
- DHC Digital Home Compliant
- Orocos-RTT Orocos Real-Time Toolkit
- ABM Agent-based modeling
- PRISM probabilistic symbolic model-checker
- ADL Architectural Description Language
- CML COMPASS Modelling Language