Based on the security case pattern provided, the instantiated security case for ACAS Xu is created below in a hierarchical tree format:

```

- G0: ACAS Xu satisfies security requirements

- S0: Argue through asset protection and secure development requirements

- C0: Describes ACAS Xu

- J0: The argumentation is based on satisfaction of SRs

- A0: System SRS are complete, adequate, and consistent

- G1: ACAS Xu satisfies the asset protection requirements

- A1: Asset inventory is established

- S1: Argue through the different stages of the system development life cycle

- G3: Asset protection requirements are met during the architecture design phase

- S2: Argue through deriving security threats from SRs

- J1: Detection and mitigation of threats fulfill SRs

- G5: ACAS Xu architecture is protected against identified security threats (STs)

- A2: All relevant threats have been identified

- S3: Argue over each security threat

- G0.1: ACAS Xu architecture is protected against ST1 (Tampering)

- G0.2: ACAS Xu architecture is protected against ST2 (Spoofing)

- G0.3: ACAS Xu architecture is protected against ST3 (Elevation of privileges)

- G6: ACAS Xu architecture is validated (undeveloped)

- C2: Describes the ACAS Xu architecture

- G4: Asset protection requirements are met during other phases (undeveloped)

- G2: ACAS Xu satisfies secure development requirements (undeveloped and uninstantiated)

- C3: Describes ACAS Xu architecture model

- A3: ACAS Xu architecture model is well defined in formal method (uninstantiated)

```

This instantiated security case represents how the ACAS Xu system addresses security requirements, particularly focusing on asset protection and secure development aspects while highlighting which components are not yet fully developed or require instantiation. Each branch of the security case reflects on goals supported by strategies that argue over and validate the approach against specific threats and requirements, accompanied by contexts, justifications, and assumptions relevant to the stages of development and operation of ACAS Xu.